

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,511,586 B2  
APPLICATION NO. : 10/550827  
DATED : March 31, 2009  
INVENTOR(S) : Mattisson

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Column 8, Line 46, Equation 5, delete “  $\sigma_{T_{eff}} \sim nV / \sqrt{W \cdot L_{eff}}$  ” and insert

--  $\sigma_{T_{eff}} \sim 2nV / \sqrt{W \cdot L_{eff}}$  --, therefor.

In Column 11, Lines 40-41, delete “transistros.” and insert -- transistors. --, therefor.

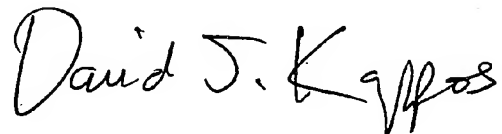
In Column 14, Line 67, in Claim 1, delete “supply:” and insert -- supply; --, therefor.

In Column 15, Line 3, in Claim 2, delete “to,” and insert -- to --, therefor.

In Column 15, Line 48, in Claim 8, delete “Gapacitor” and insert -- capacitor --, therefor.

Signed and Sealed this

Twenty-seventh Day of October, 2009



David J. Kappos  
*Director of the United States Patent and Trademark Office*